

Attorney Docket No. CA7010223001



Group Art Unit: 2825

Examiner: Whitmore, Stacy

Confirmation No.: 5787

For: METHOD AND SYSTEM FOR FACILITATING ELECTRONIC CIRCUIT AND CHIP DESIGN USING REMOTELY LOCATED RESOURCES

REQUEST FOR CONTINUED EXAMINATION UNDER 37 C.F.R. 1.114 WITH
AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed on July 5, 2006 and the Advisory Action mailed on September 26, 2006, Applicants make a request for continued examination under 37 C.F.R. § 1.114 and amend the above-identified application as follows.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 11 of this paper.